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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2825

Examiner: Thompson, A.

Applicant: James H. Lie

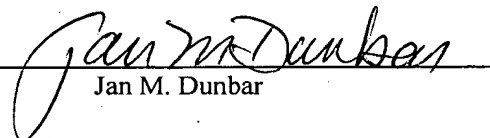
Serial No: 09/940,749

Filing Date: August 28, 2001

For: METHOD AND/OR ARCHITECTURE FOR GENERATING  
SUPERSET PINOUT FOR DEVICES WITH COMBINED  
PROGRAMMABLE LOGIC AND HIGH-SPEED SERIAL CHANNELS

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 27, 2004.

By:

  
Jan M. Dunbar

**NOTICE OF APPEAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The Applicant of the above-captioned patent application hereby appeals to the Board of Patent Appeals and Interferences from the decision dated May 12, 2004 of the Examiner finally rejecting Claims 1-4, 6-11 and 13-20.

An extension of time to respond to the final rejection is hereby requested for two months. The payment for the two month extension fee and the appeal fee is enclosed herewith.

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If Applicant has not requested a sufficient extension and/or has not paid a sufficient fee for this matter, and/or for the extension necessary to prevent the abandonment of this application, please consider this as a request for an extension for the required time period and/or authorization to charge our Deposit Account No. 50-0541 for any fee which may be due.

Respectfully submitted,

By: Robert M. Miller  
Robert M. Miller  
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Date: May 27, 2004

Attorney Docket No.: 0325.00503